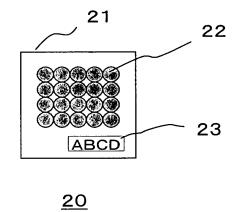
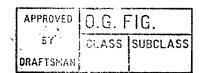


FIG.2







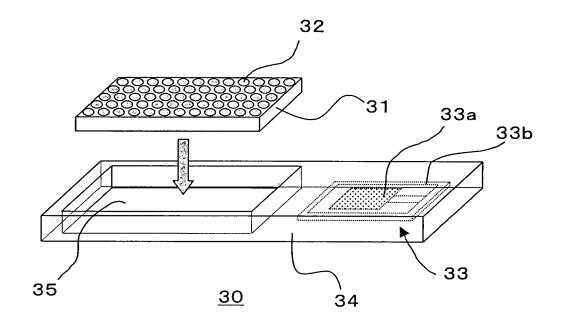
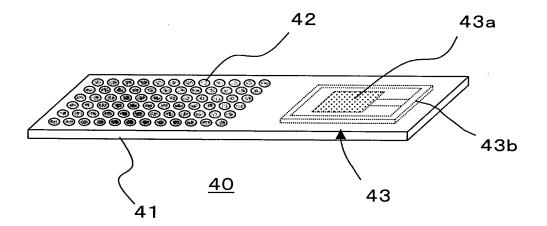


FIG.4



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FIG.

Chip number			
Fabrication lot number	ot number		
Date/time of preparation	preparation		
Number of spots (n)	oots (n)		
Additional chip information	ip information		
Spot No. (1)	Spot No. (1)   Spotting location [coordinates (x1,y1)]   Spotting condition information (1)   Additional spotting information (1)	Spotting condition information (1)	Additional spotting information (1)
(2)	[coordinates (x2,y2)]	(2)	(2)
(n)	[coordinates (xn,yn)]	(u)	(2)

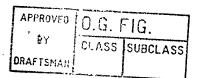
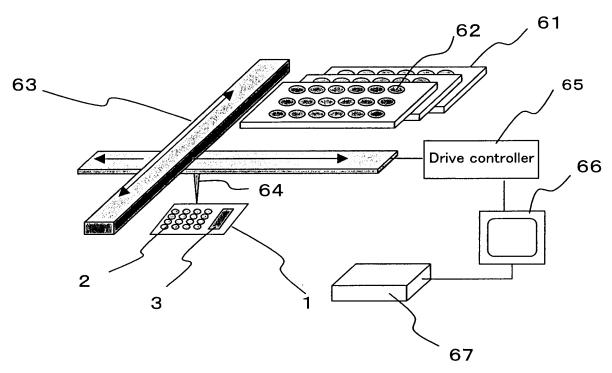
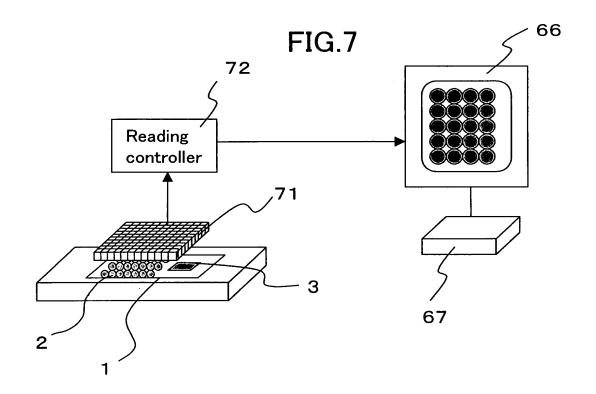


FIG.6





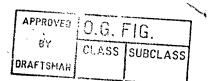
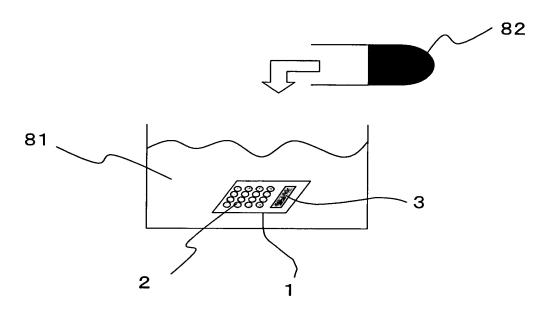
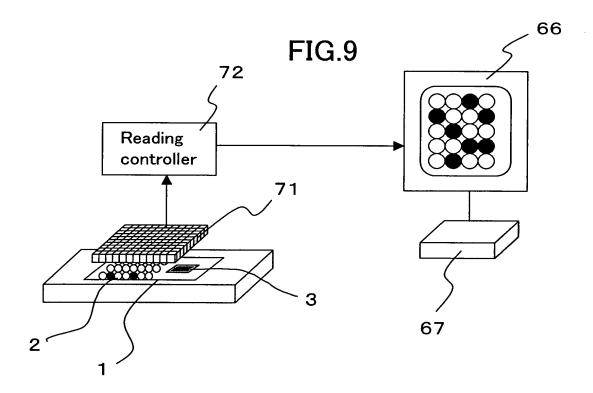


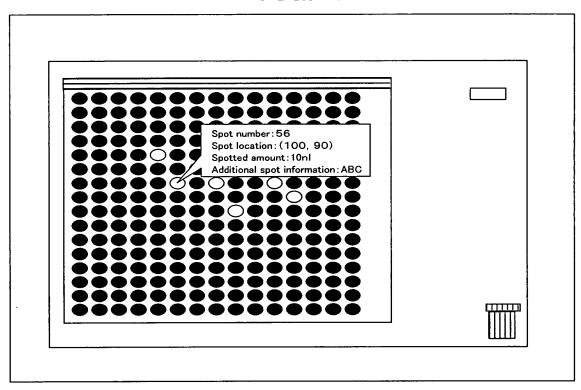
FIG.8

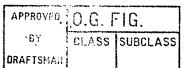




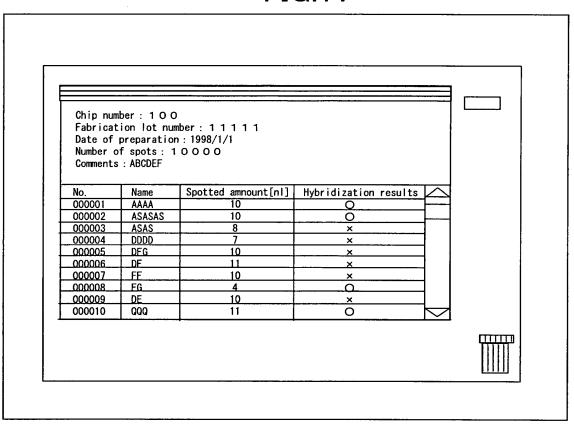
APPROVED O.G. FIG.
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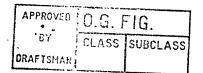
FIG.10



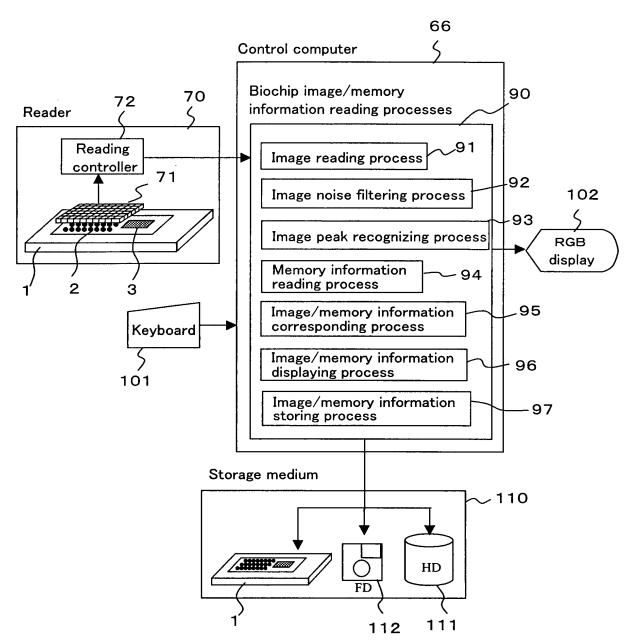


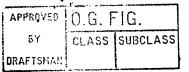






## **FIG.12**





**FIG.13** 

